Electronic Acknowledgement Receipt				
EFS ID:	1966858			
Application Number:	10678055			
International Application Number:				
Confirmation Number:	4569			
Title of Invention:	Method and apparatus for reducing stress across capacitors used in integrated circuits			
First Named Inventor/Applicant Name:	Ramkarthik Ganesan			
Customer Number:	8791			
Filer:	William W. Schaal/Susan McFarlane			
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Attorney Docket Number:	42P6963R			
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File Listing:

Document Number	Document Description	File Name	File Size(Bytes) /Message Digest	Multi Part /.zip	Pages (if appl.)			
1	Applicant summary of interview with examiner	P6963R.PDF	28684	no	1			
			4f84a87379824fc4c66dbf8c1848650cef a56f15					
Warnings:								

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If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.

New International Application Filed with the USPTO as a Receiving Office

If a new international application is being filed and the international application includes the necessary components for an international filing date (see PCT Article 11 and MPEP 1810), a Notification of the International Application Number and of the International Filing Date (Form PCT/RO/105) will be issued in due course, subject to prescriptions concerning national security, and the date shown on this Acknowledgement Receipt will establish the international filing date of the application.